Amendments to the Specification

Please amend paragraphs 2-4 as indicated below:

[0002] A register system is a key component of a microprocessor. The register system should be ~~response~~ responsible and able to deliver data quickly, yet be large enough to support a high level of instruction level parallelism (ILP).

[0003] Microprocessors increasingly require fast register files to reduce cycles spent in register read. Each additional cycle spent in register read increases branch misprediction latency. Furthermore, speculative schedulers need a fast register file to reduce the time from ~~schedule~~ scheduling to execution~~execute~~.

[0004] A register file's size determines, in a large part, the effective limits of the number of in-flight instructions, and the amount of extractable ILP. Increases in register file size, however, ~~results~~ result in a slower structure that requires more power and microprocessor complexity~~, for example, the number of ports,~~ to operate. Increased complexity may include, for example, increased number of ports.